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BY EXPRESS MAIL ON
June 1, 2001
(EK 825 217 138 US)

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Box PATENT APPLICATION
ASSISTANT COMMISSIONER FOR PATENTS
Washington, DC 20231

RECORD OF TRANSMITTAL

INVENTORS: (1) Hirofumi HARADA

TITLE: VERTICAL MOS TRANSISTOR AND A METHOD OF MANUFACTURING
THE SAME

DOCKET NO: S004-4310

S I R:

Kindly find enclosed herewith for filing the documents
checked below. Any fees or charges not covered by the
accompanying check should be charged to our Deposit Account
No. 01-0268.

APPLICABLE ITEMS CHECKED:

XX PATENT APPLICATION (pages 1-21 of specification, 9
claims (pages) 18-21 and abstract of the
disclosure, (page) 22).

DESIGN APPLICATION (specification, claim, declaration and
power of attorney and photograph(s))

XX FORMAL DRAWINGS (5 sheets) (NI A4)

INFORMAL DRAWINGS (sheets)

RECORDATION FORM COVER SHEET and ASSIGNMENT
(to be recorded and returned to the undersigned)

XX PRIORITY CLAIM - applicant(s) claim the benefit of
priority of the following application(s):

Japanese Patent Appln. No. 2000-166214 filed June 2, 2000

XX PRIORITY DOCUMENT(s)

 Enclosed XX Not Enclosed

 VERIFIED STATEMENT TO ESTABLISH SMALL ENTITY STATUS

XX EXPRESS MAIL CERTIFICATION

 OTHER -

XX FEE

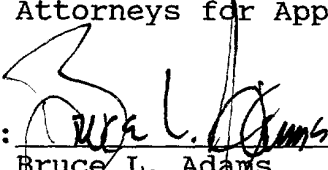
A check is enclosed for the following official fees:

	Large Entity	Small Entity
<u>XX</u> Basic Filing Fee	\$ 710.00	\$
<u>0</u> Independent Claims in Excess of 3	\$	\$
<u>0</u> Total Claims in Excess of 20	\$	\$
<u> </u> Multiple Claim Fee	\$	\$
<u> </u> Assignment Recordal Fee	\$	\$
TOTAL	\$ 710.00	\$

Respectfully submitted,

ADAMS & WILKS
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By: 
Bruce L. Adams
Reg. No. 25,386

Re: New patent application of Hirofumi HARADA

for VERTICAL MOS TRANSISTOR AND A METHOD OF MANUFACTURING THE
SAME

comprising transmittal letter, title, specification (pgs.1-21),
9 claims (pgs.18-21), abstract of the disclosure (pg.22), 5
sheets of formal drawings containing thereon Figs. 1-11, express
mail certification and check in the amount \$710.00

Attorney's Docket No: S004-4310

EXPRESS MAIL CERTIFICATION UNDER 37 CFR §1.10

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Michael Ruas